

## References

- [1] Fred S. Annexstein. Parallel implementations of graph embeddings. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 207–217. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [2] Patrice Bertin, Didier Roncin, and Jean Vuillemin. Programmable active memories: A performance assessment. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 119–130. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [3] Michel Cosnard and Pascal Koiran. Relations between models of parallel abstract machines. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 37–46. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [4] Sergio Felperin, Prabhakar Raghavan, and Eli Upfal. An experimental study of wormhole routing in parallel computers. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 156–165. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [5] Arno Formella, Werner Massonne, and Wolfgang J. Paul. Cost effectiveness of data flow machines and vector processors. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678

of *LNCS*, pages 48–65. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.

- [6] Mohammad H. Heydari and I. Hal Sudborough. On sorting by prefix reversals and the diameter of pancake networks. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 218–227. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [7] S. Lennart Johnsson. Massively parallel computing: Data distribution and communication. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 68–92. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [8] Tom Leighton and Bruce Maggs. The role of randomness in the design of interconnection networks. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 131–145. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [9] Charles E. Leiserson. The networks of the connection machine cm-5. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 66–67. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [10] Fabrizio Luccio, Linda Pagli, and Geppino Pucci. Three non conventional paradigms of parallel computation. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages

166–175. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.

- [11] Ernst W. Mayr and Ralph Werchner. Optimal implementation of general divide-and-conquer on the hypercube and related networks. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 195–206. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [12] Friedhelm Meyer auf der Heide. Hashing strategies for simulating shared memory on distributed memory machines. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 20–29. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [13] Burkhard Monien, Reinhard Lüling, and Falk Langhammer. A realizable efficient parallel architecture. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 93–109. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [14] Franco P. Preparata. Frontiers of parallel computing. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, page 47. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [15] Abhiram Ranade. A framework for analyzing locality and portability issues in parallel computing. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 185–194. Springer-

Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.

- [16] Arnold L. Rosenberg. Better parallel architectures via emulations. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 30–36. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [17] Larry Rudolph. Hardware support for collective communication operations. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 110–118. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [18] Marc Snir. Scalable parallel computers and scalable parallel codes: From theory to practice. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 176–184. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [19] Leslie G. Valiant. A combining mechanism for parallel computers. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 1–10. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.
- [20] Uzi Vishkin. A case for the pram as a standard programmer’s model. In F. Meyer auf der Heide, B. Monien, and A.L. Rosenberg, editors, *Proceedings of the 1st Heinz Nixdorf Symposium on Parallel Architectures and Their Efficient Use (Paderborn, Germany, November 1992)*, volume 678 of *LNCS*, pages 11–19. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong-Barcelona-Budapest, 1993.