References

[1] Carme Àlvarez, José L. Balcázar, Joaquim Gabarró, and Miklós Sántha. Parallel complexity in the design and analysis of concurrent systems. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 288–303. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[2] M. Baumslag, M.C. Heydemann, J. Opatrny, and D. Sotteau. Embeddings of shuffle-like graphs in hypercubes. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 179–190. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[3] Sajal K. Das and Wen-Bing Horng. Managing a parallel heap efficiently. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 270–287. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[4] Pilar de la Torre and Clyde P. Kruskal. Towards a single model of efficient computation in real parallel machines. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 6–24. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[5] J. Duato. On the design of deadlock-free adaptive routing algorithms for multicomputers: Design methodologies. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International*

Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991), volume 505 of *LNCS*, pages 390–405. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[6] Pavel Ferianc and Ondrej Sýkora. Area complexity of multilective merging. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 209–218. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[7] Jean Marie Filloque, Eric Gautrin, and Bernard Pottier. Efficient global computations on a processor network with programmable logic. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 69–82. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[8] Guang R. Gao, Herbert H.J. Hum, and Jean-Marc Monti. Towards an efficient hybrid dataflow architecture model. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 355–371. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[9] R. Govindarajan and Sheng Yu. Data flow implementation of generalized guarded commands. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 372–389. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[10] T. Hagerup, A. Schmitt, and H. Seidl. Fork: A high-level language for prams. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors,

Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991), volume 505 of *LNCS*, pages 304–320. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[11] Philippe Hoogvorst, Ronan Keryell, Philippe Matherat, and Nicolas Paris. Pomp or how to design a massively parallel machine with small developments. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 83–100. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[12] Herbert H.J. Hum and Guang R. Gao. A novel high-speed memory organization for fine-grain multi-thread computing. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 34–51. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[13] A.R. Hurson, B. Jín, and S.H. Pakzad. Neural network-based decision making for large incomplete databases. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 321–337. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[14] Hermann Ilmberger and Sabine Thürmel. A toolkit for debugging parallel lisp programs. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 406–422. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[15] Thomas Johnsson. Parallel evaluation of functional programs: The $\langle \nu, g \rangle$-machine approach. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 1–5. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[16] Jan H.M. Korst, Emile H.L. Aarts, Jan Karel Lenstra, and Jaap Wessels. Periodic multiprocessor scheduling. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 166–178. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[17] K.G. Langendoen, H.L. Muller, and L.O. Hertzberger. Evaluation of futurebus hierarchical caching. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 52–68. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[18] Ahmed Louri. An optical content-addressable parallel processor for fast searching and retrieving. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 338–354. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[19] R. Milikowski and W.G. Vree. The g-line: A distributed processor for graph reduction. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 119–136. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[20] C. Mongenet. Affine timings for systems of affine recurrence equations. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 236–251. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[21] Ian Parberry. On the computational complexity of optimal sorting network verification. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 252–269. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[22] José M. Piquer. Indirect reference counting: A distributed garbage collection algorithm. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 150–165. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[23] Gerard Tel and Friedemann Mattern. The derivation of distributed termination detection algorithms from garbage collection schemes. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 137–149. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[24] Philip C. Treleaven. Neural computing and the galatea project. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 25–

33. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[25] Vincent van Dongen. Mapping uniform recurrences onto small size arrays. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 191–208. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[26] Jonas Vasell and Jesper Vasell. The function processor: An architecture for efficient execution of recursive functions. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 101–118. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.

[27] Xiaoxiong Zhong and Sanjay Rajopadhye. Deriving fully efficient systolic arrays by quasi-linear allocation functions. In E.H.L. Aarts, J. van Leeuwen, and M. Rem, editors, *Proceedings of the 3rd International Conference on Parallel Architectures and Languages Europe, Vol. I: Parallel Architectures and Algorithms, PARLE'91 (Eindhoven, The Netherlands, June 1991)*, volume 505 of *LNCS*, pages 219–235. Springer-Verlag, Berlin-Heidelberg-New York-London-Paris-Tokyo-Hong Kong, 1991.